

## CLAIMS:

1. A digital data filtering circuit able to implement the steps of:

- calculating a discrete transform (DCT2N) of a set of 8 original data (w),
- calculating an inverse discrete transform (IDCT2N) of the set of transformed data (W) thus obtained,

5 said circuit being able to filter at least one data item among the set of transformed data (W), and being characterized in that it comprises:

- a first filtering module (FILO1) intended to filter the odd transformed data item or the 3 odd transformed data items having the highest frequencies in the set of transformed data (W),

10 - a second filtering module (FILO2) connected to the first filtering module and intended to filter the 2 odd transformed data items having the highest frequency in the set of transformed data (W).

2. A filtering circuit as claimed in Claim 1, comprising discrete transform means  
15 (DCTN) intended to successively transform a first half (u) of the set of original data and a second half (v) of the set of original data, said circuit also comprising a third filtering module (FILE) intended to filter the even transformed data item or the 2 even transformed data items having the highest frequency in the set of transformed data (W) using part of the first and  
20 second transformed data halves (U,V).

3. A filtering circuit as claimed in Claim 1 or 2, where half of the data in the set of original data are data of even or odd rank in a segment of a first coding block and half are data with the same parity as a corresponding segment of a second coding block adjacent to the first coding block.

25 4. A filtering circuit as claimed in Claim 1 or 2, where half of the data in the set of original data are 4 data with the highest ranks in a segment of a first coding block and half are 4 data with the lowest ranks in a corresponding segment of a second coding block adjacent to the first coding block.



5. A video decoder able to supply decoded digital images and comprising a filtering circuit as claimed in one of Claims 1 to 4, able to filter the decoded digital images so as to supply filtered digital images.

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6. A portable apparatus comprising a video decoder as claimed in Claim 5, able to display the processed digital images on a screen of said apparatus.

7. A television receiver comprising a filtering circuit as claimed in any one of  
10 Claims 1 to 4, able to filter digital images received by said receiver so as to display filtered digital images on a screen of said receiver.